

Fig 1

Transmit 201

Receive 202

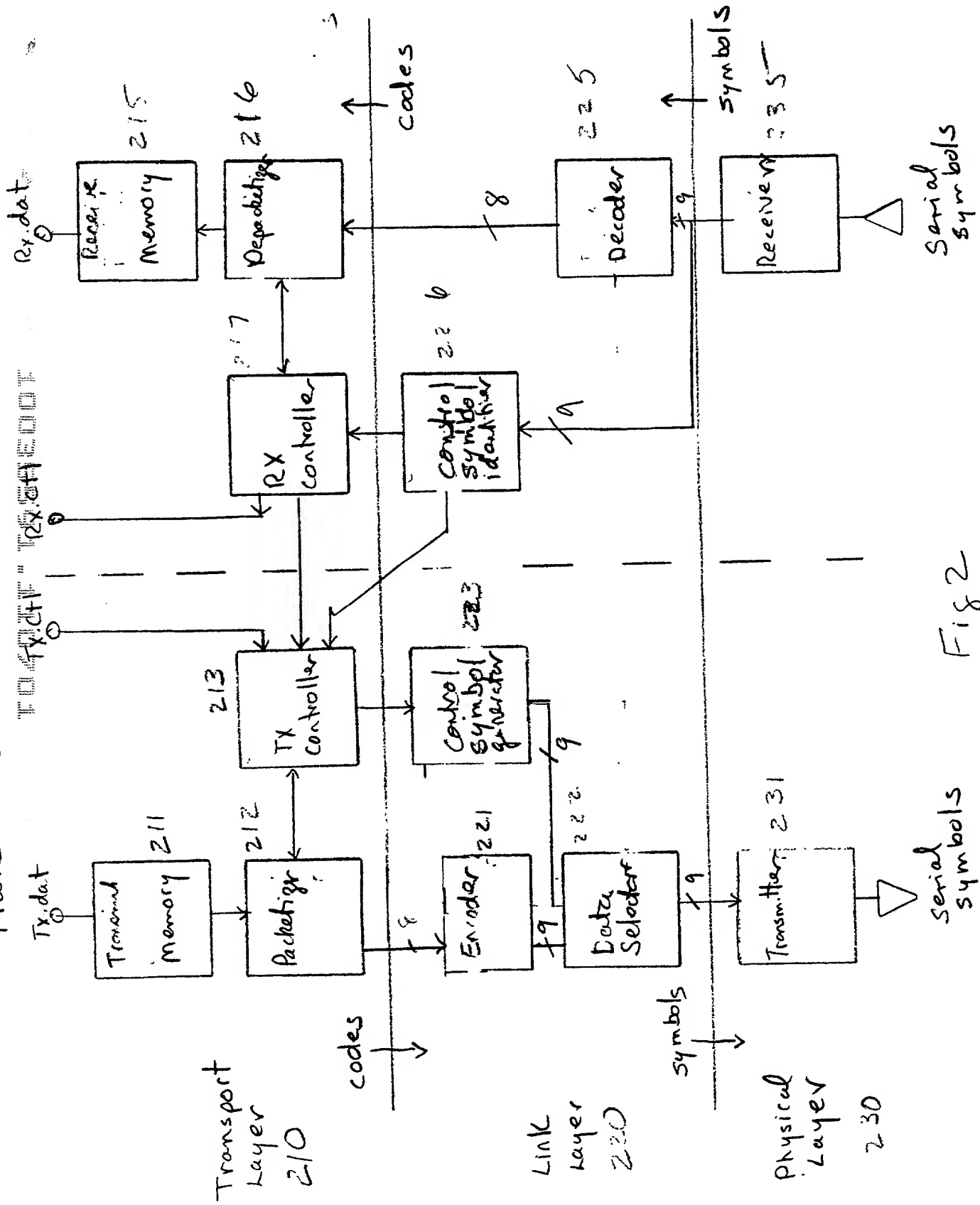


Fig 2

Physical Layer

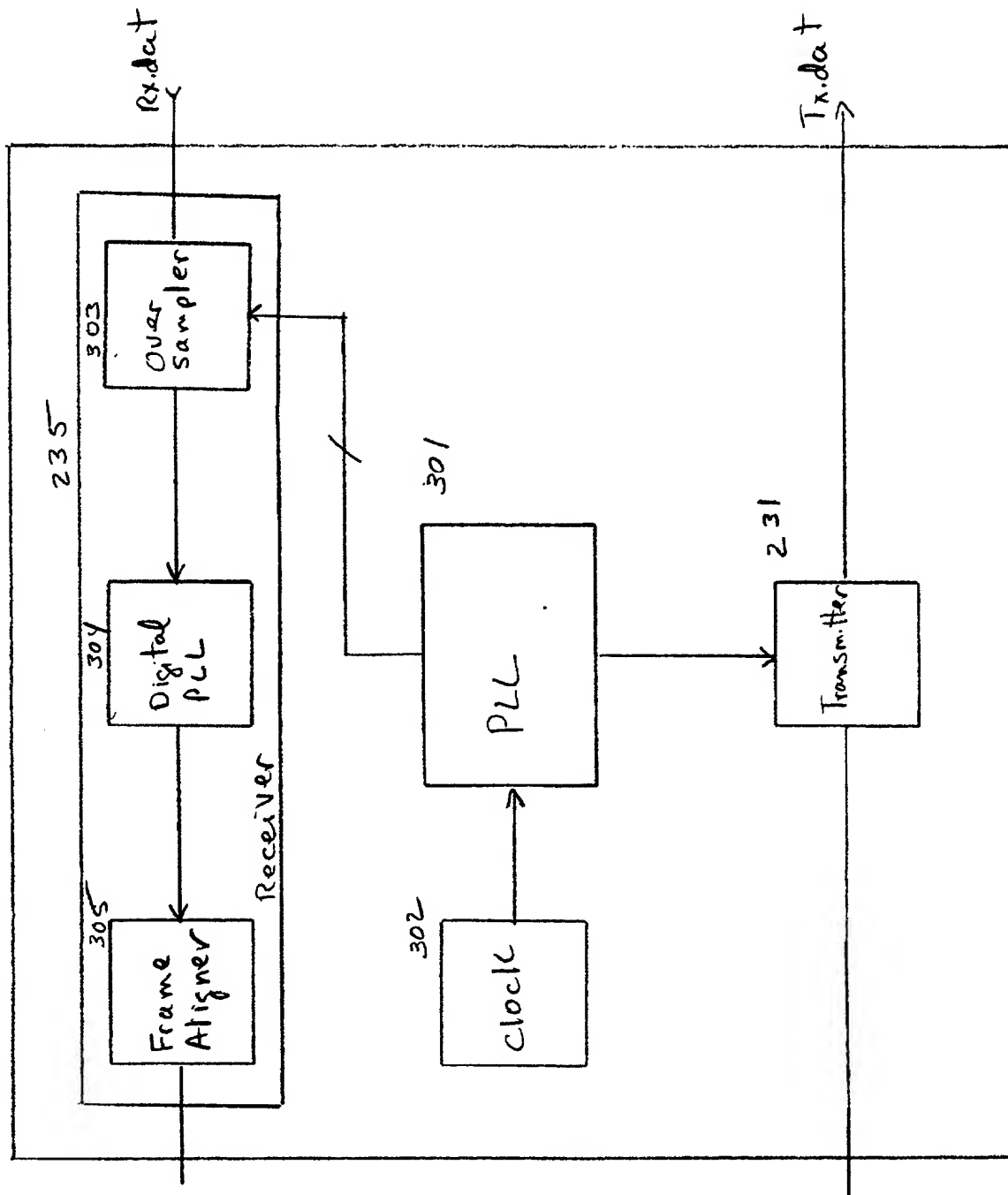


Fig 3

Packet

400

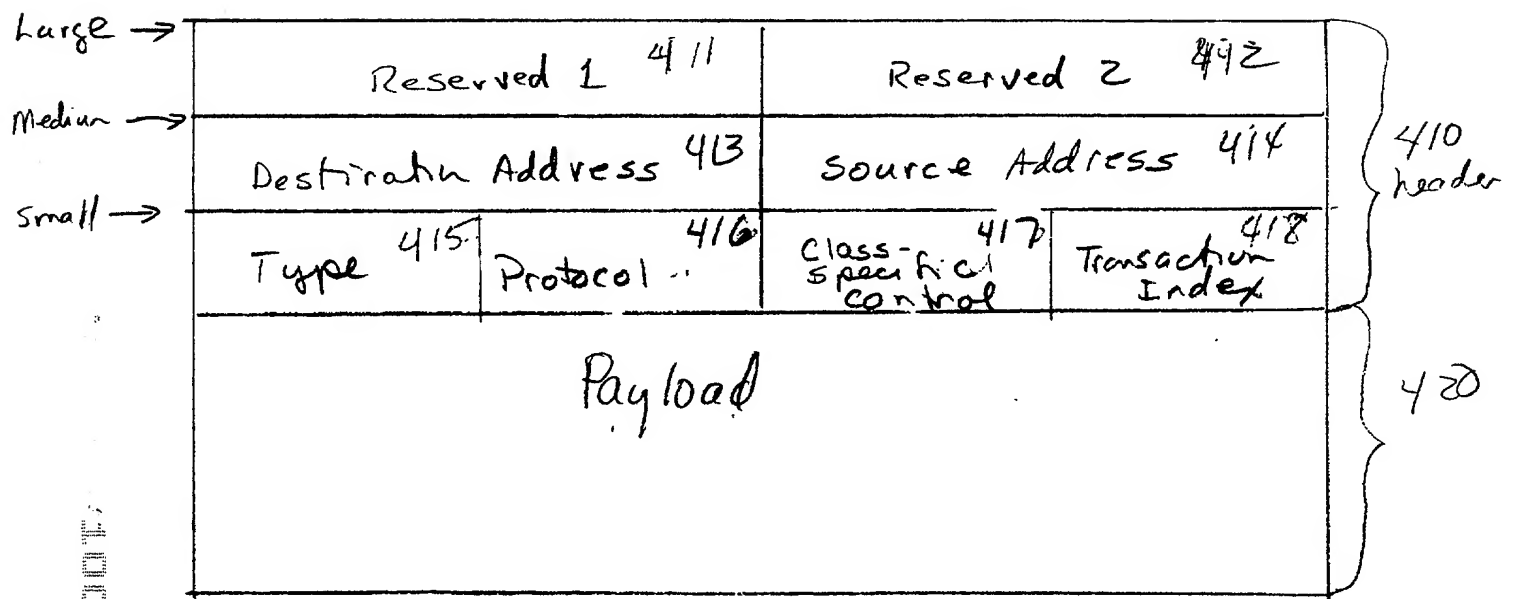
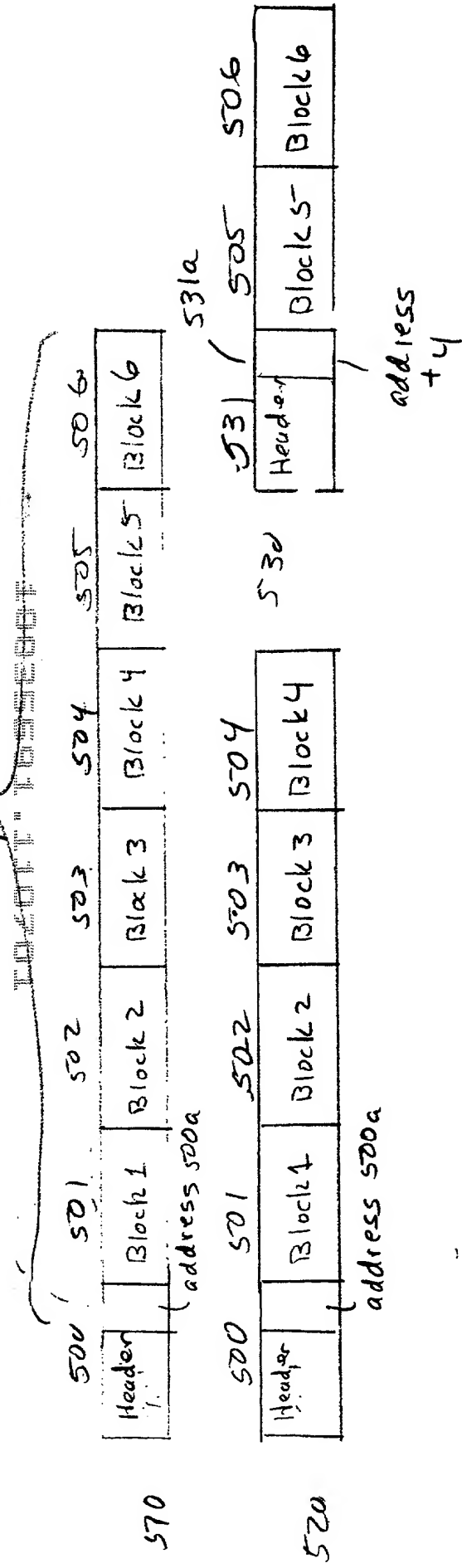
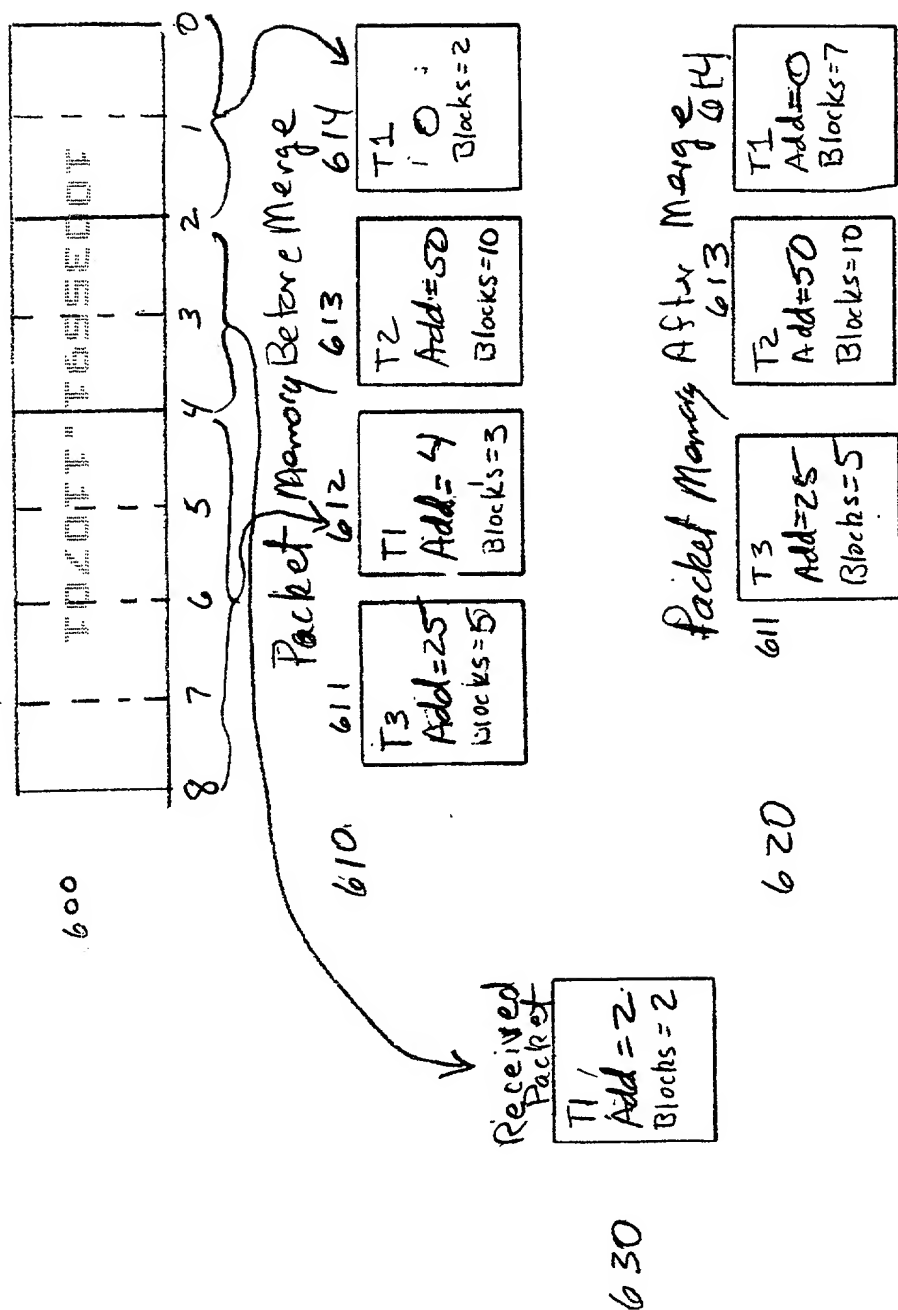


Fig 4

Payload 511



5.85



F. 86

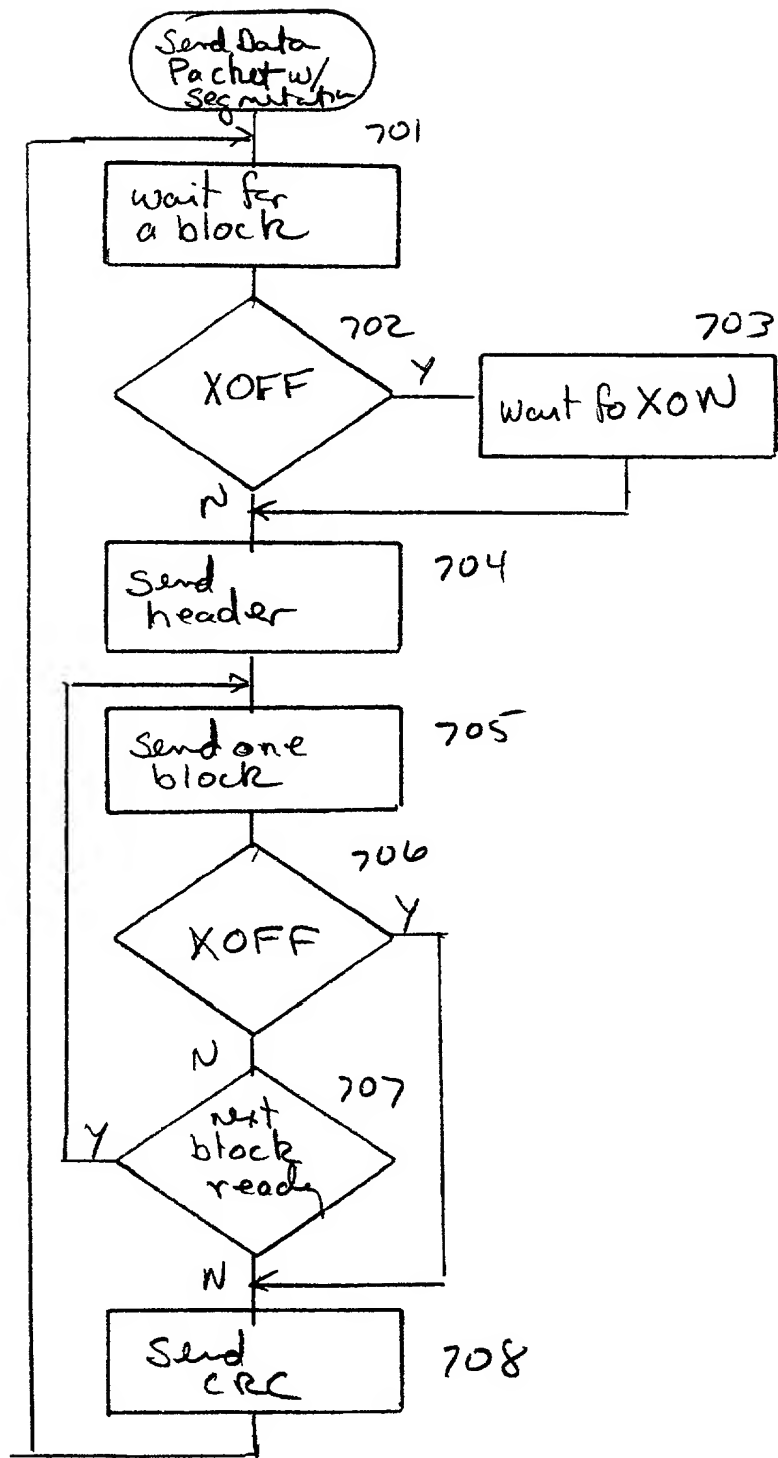
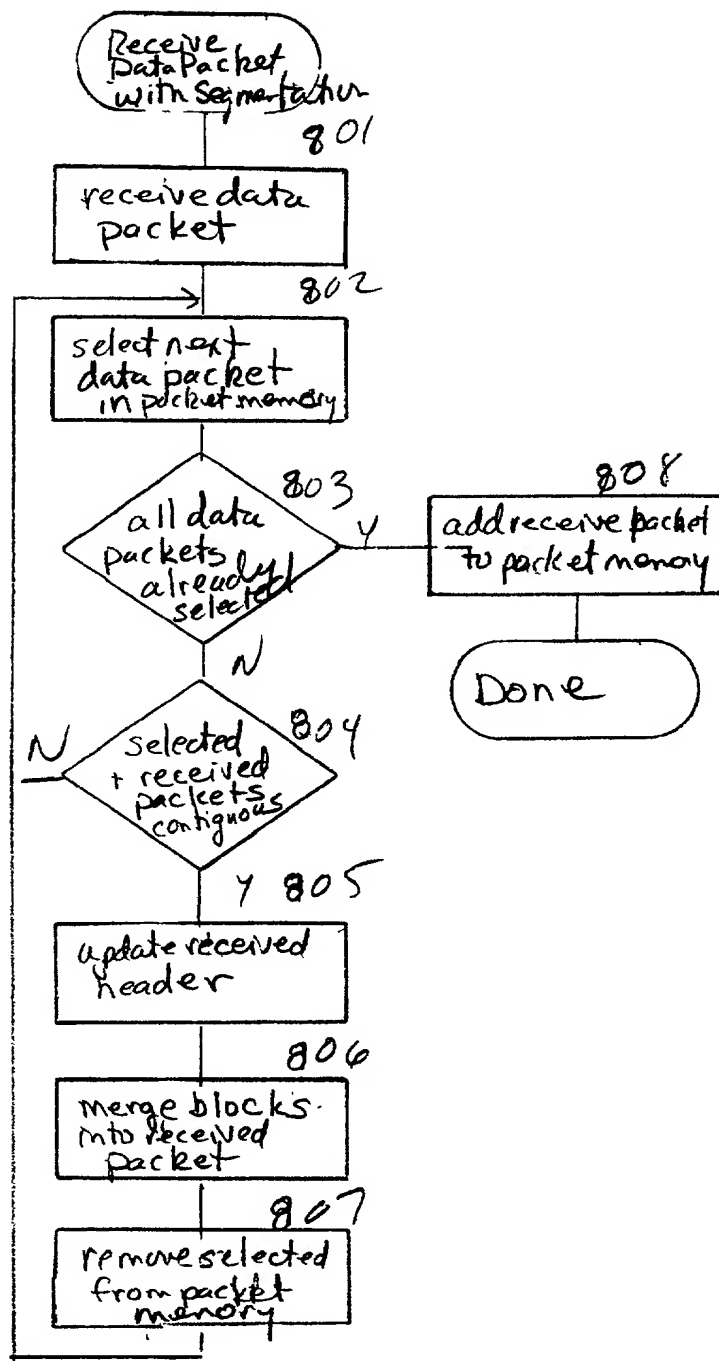
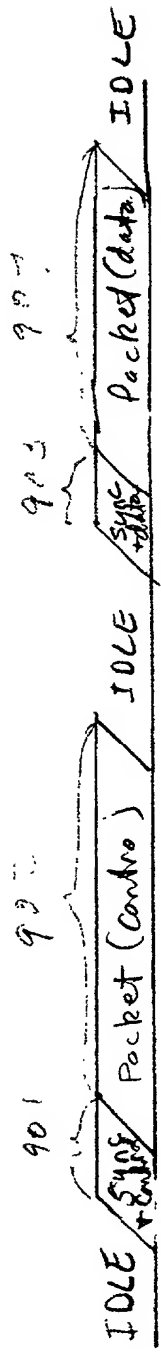


Fig 7



Fig



sync + packet type

Fig 9A

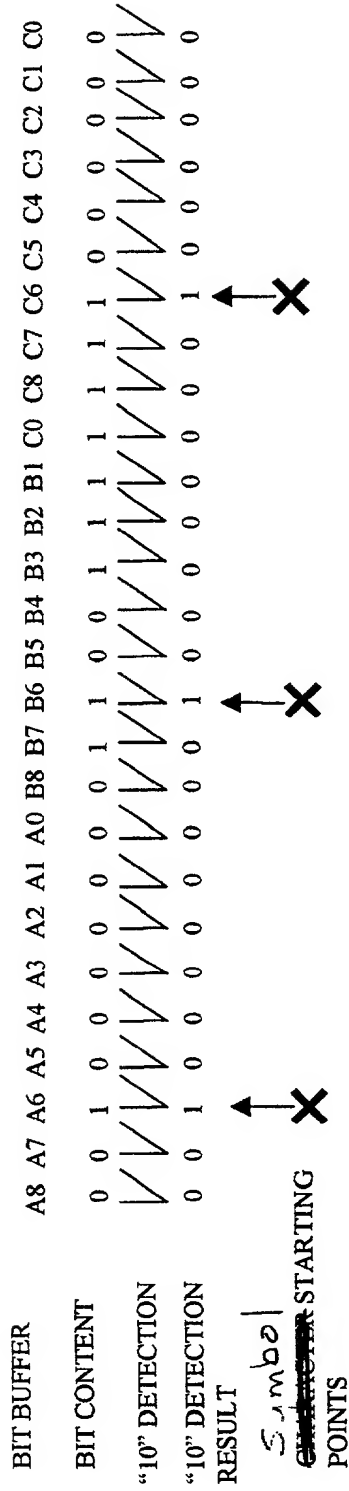


FIG.10

Fig 9B

910

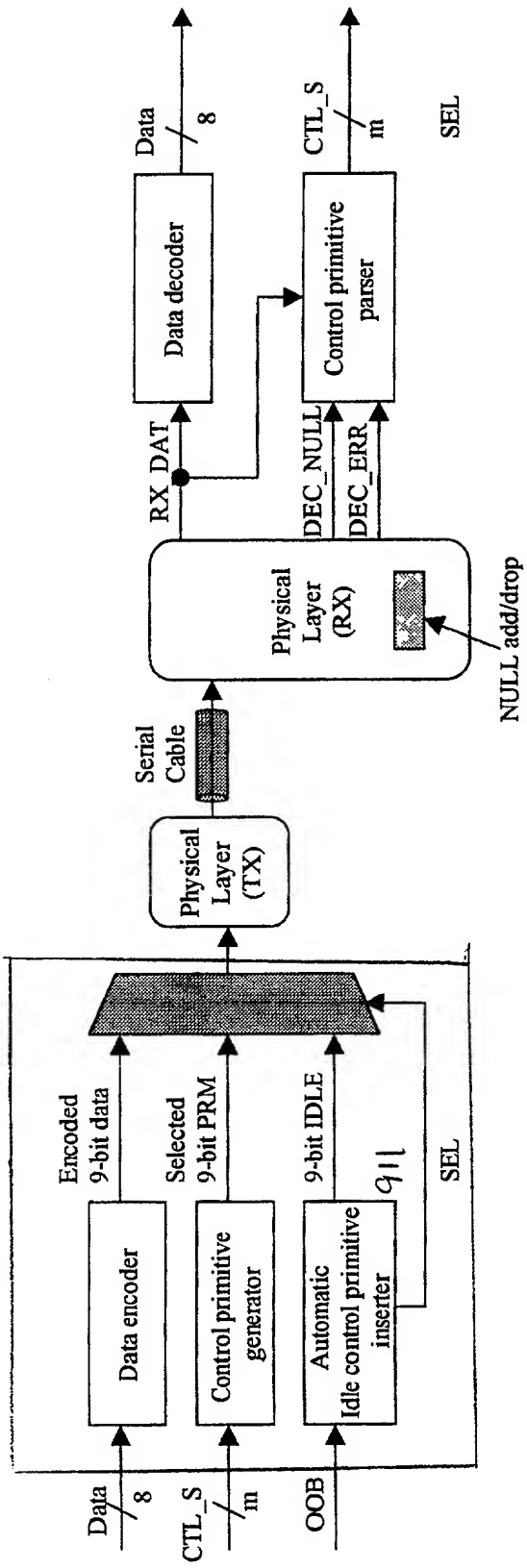


Fig. 9C

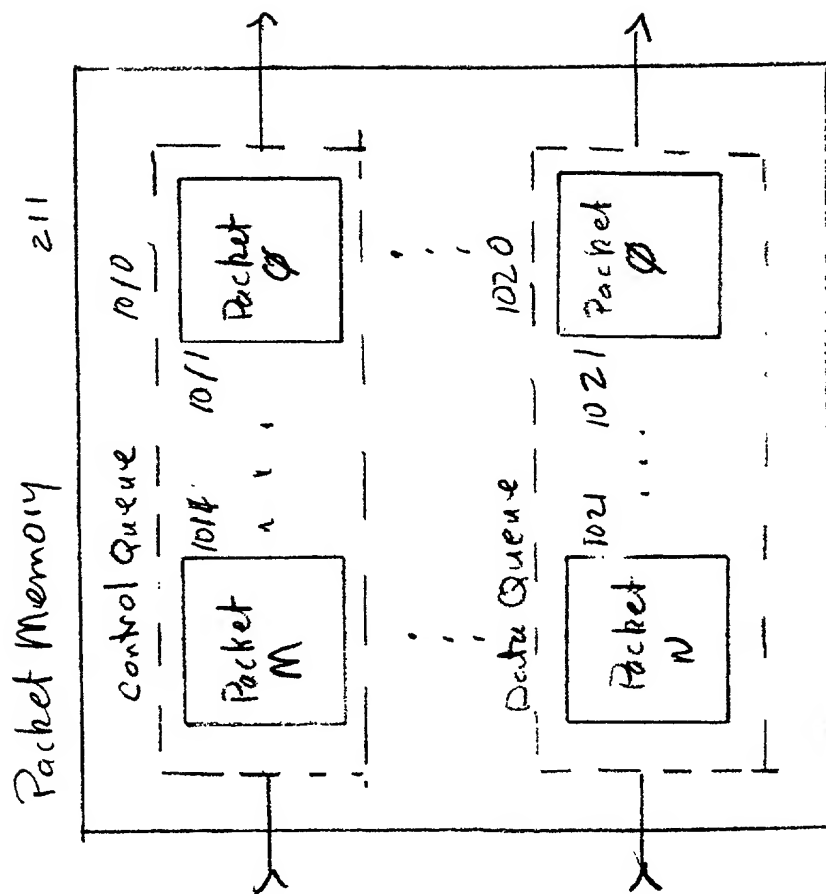


Fig 10

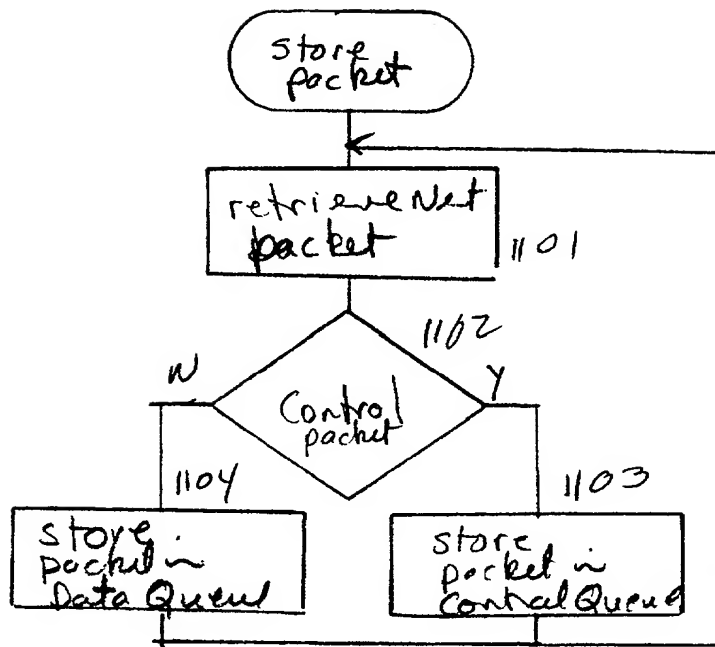


Fig 11

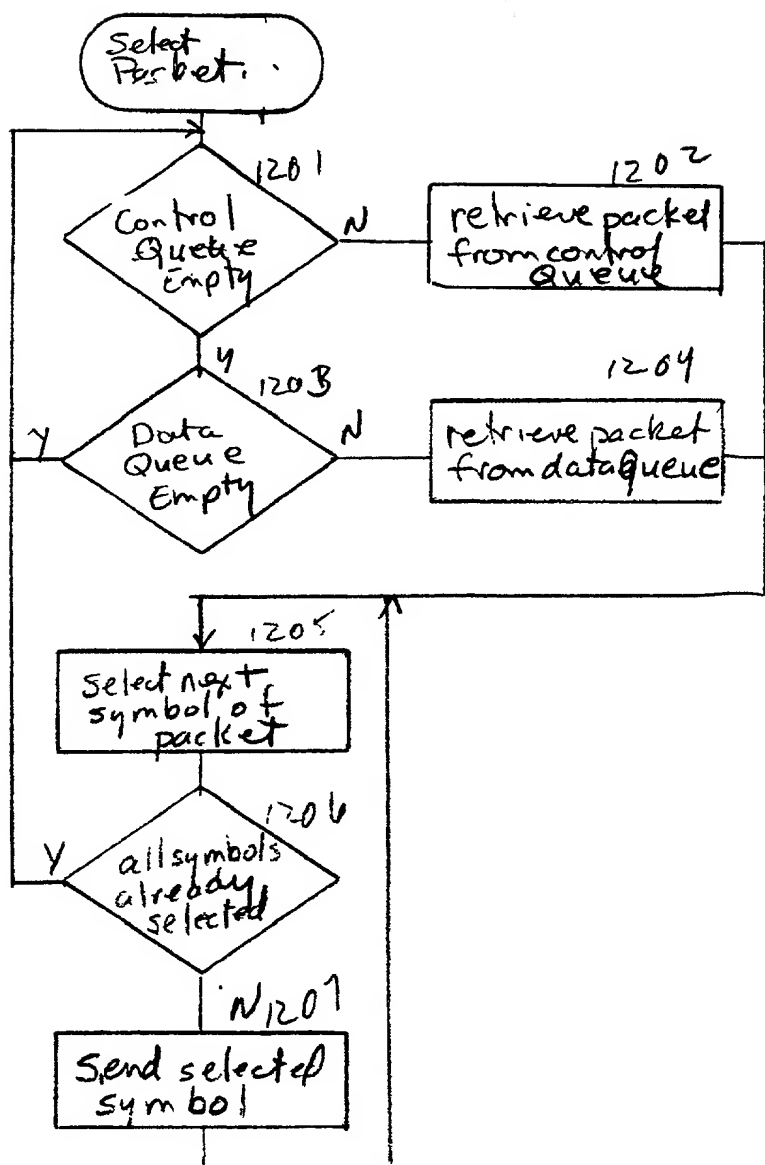


Fig 12

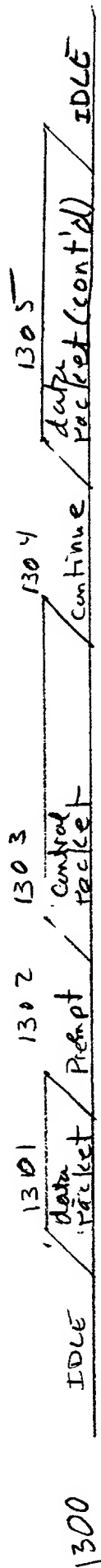


Fig 13

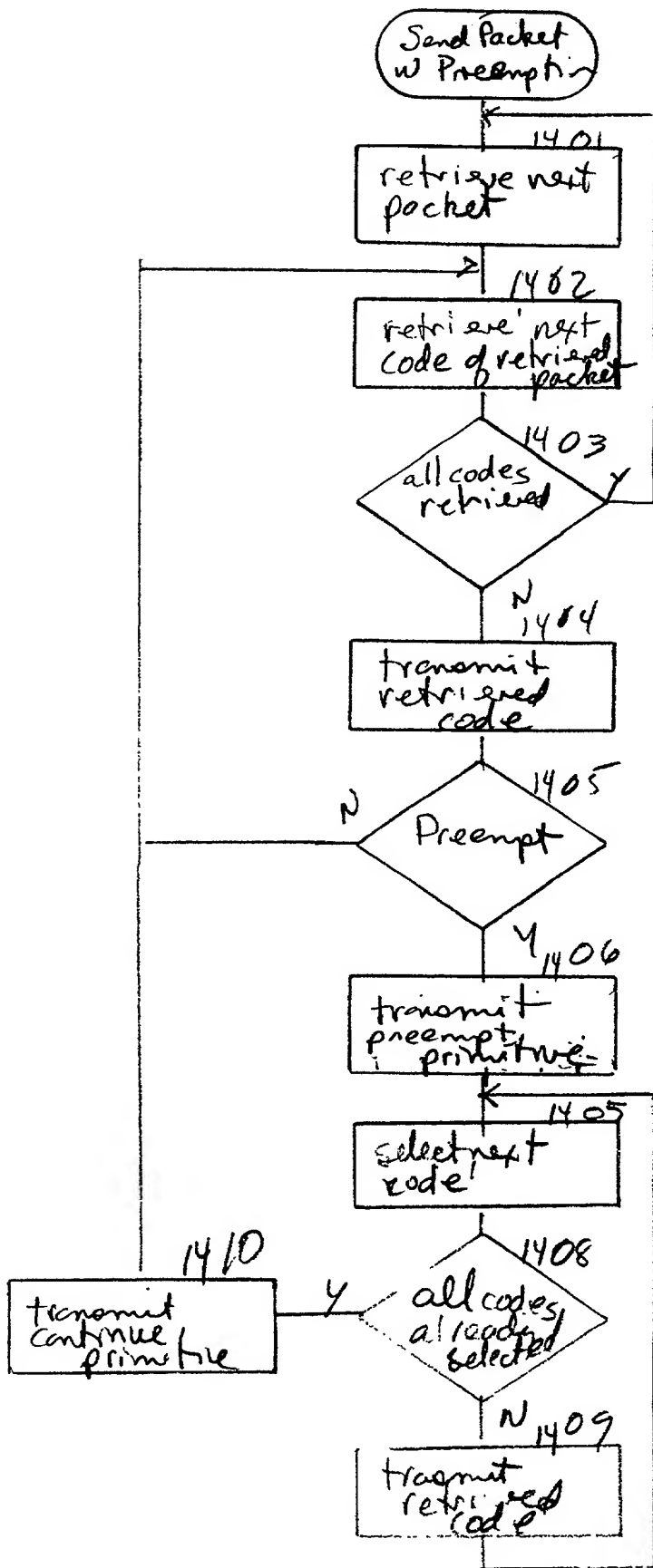


Fig 14

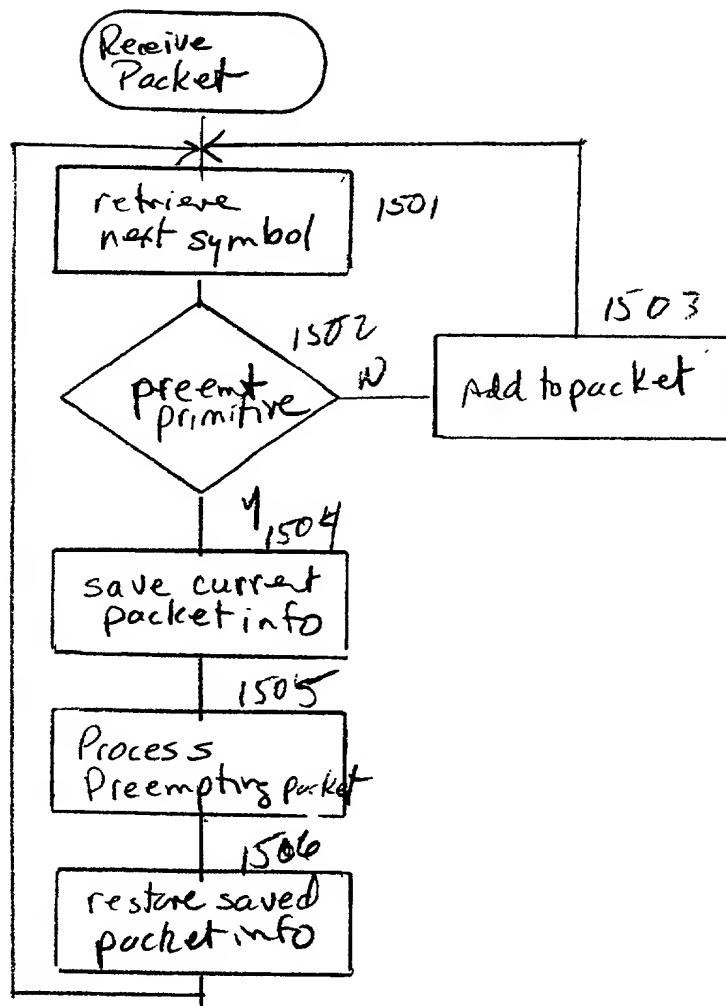


Fig 15

Switch Network 1630

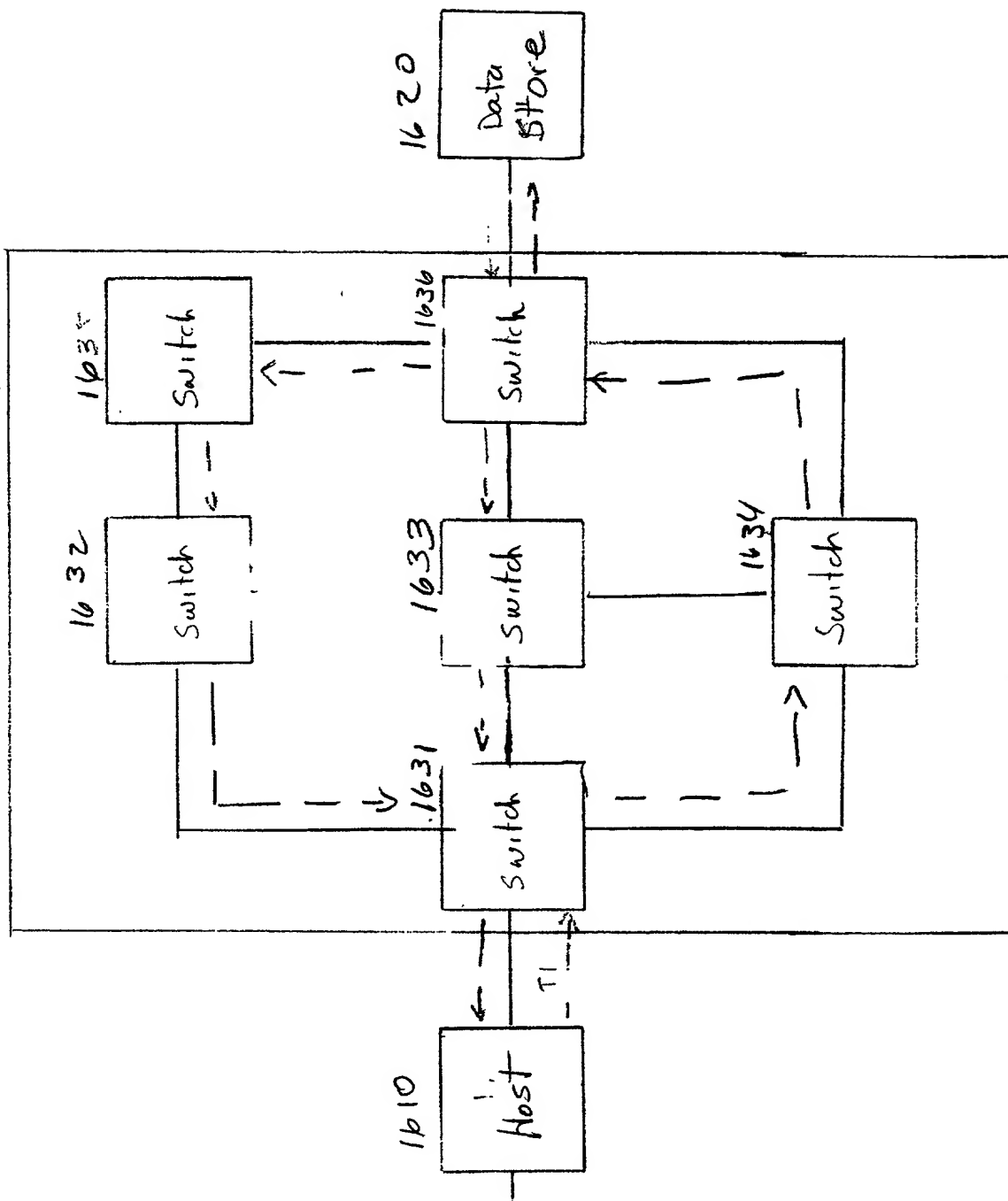
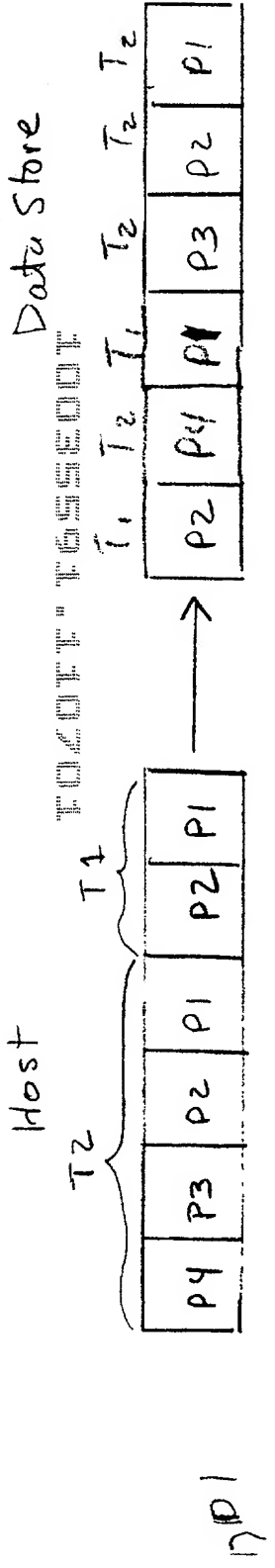
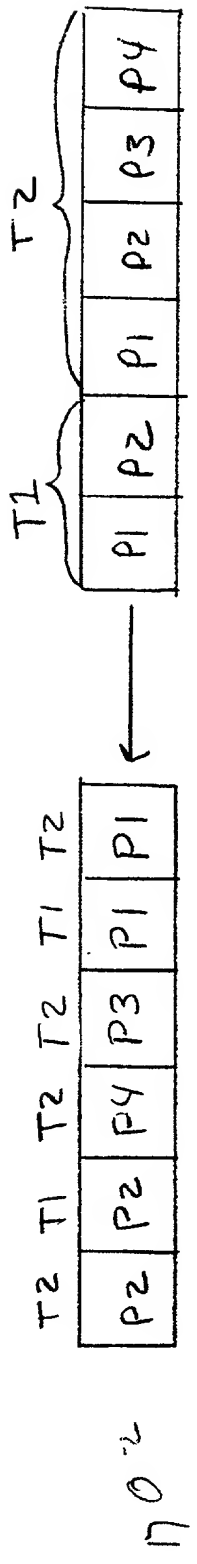


Fig 16



Preserving Packet Order w/ Transaction



No Packet or Transaction Ordering

Fig 17

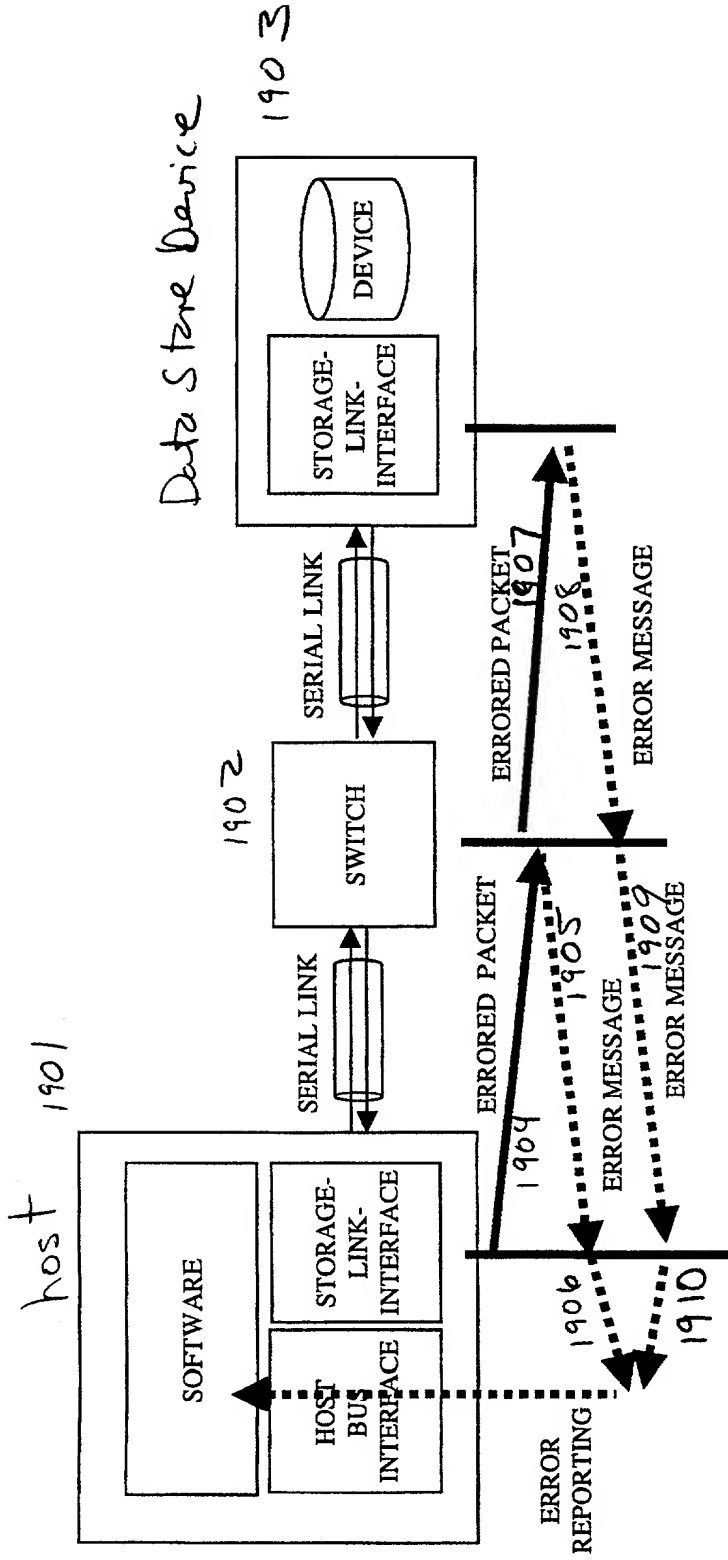


Fig 19A

1901A

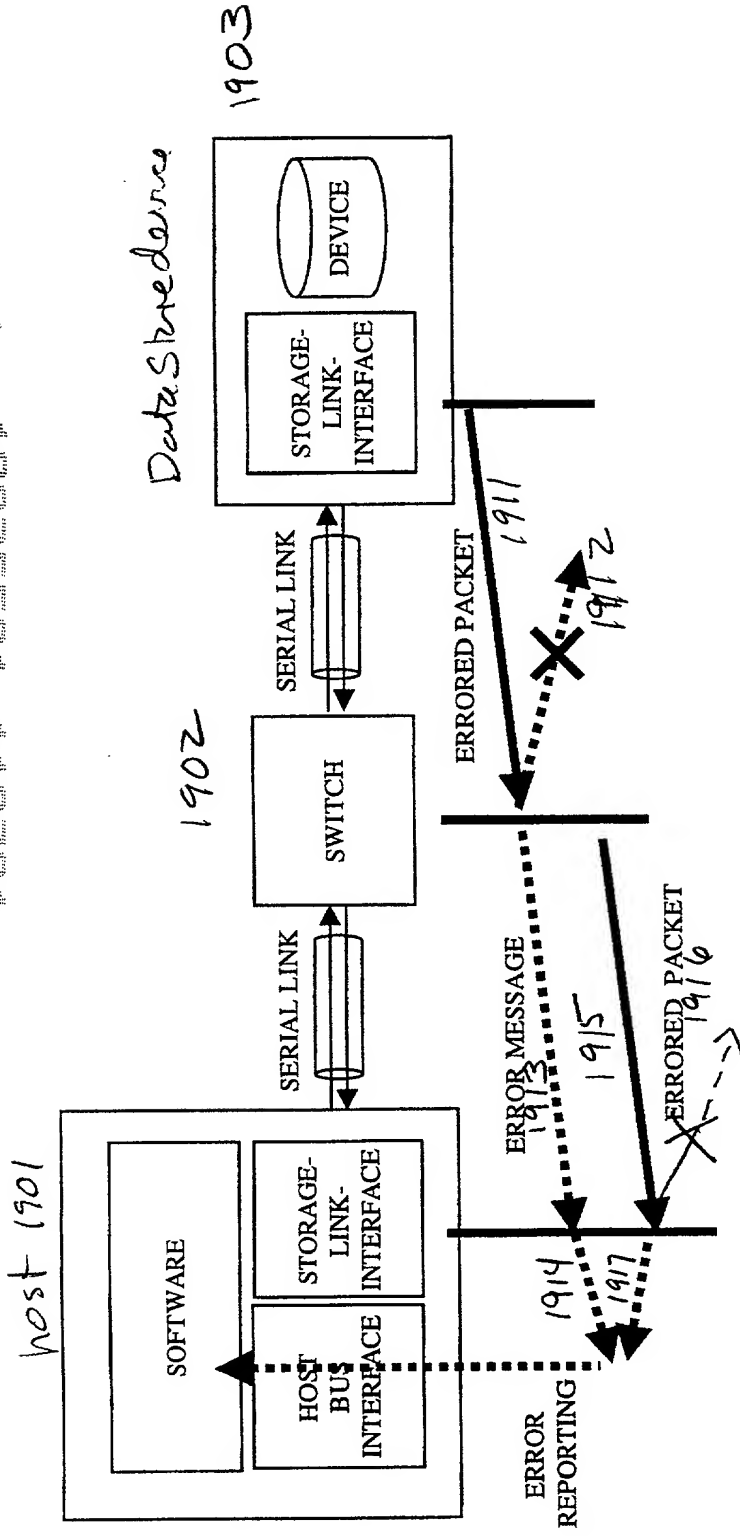
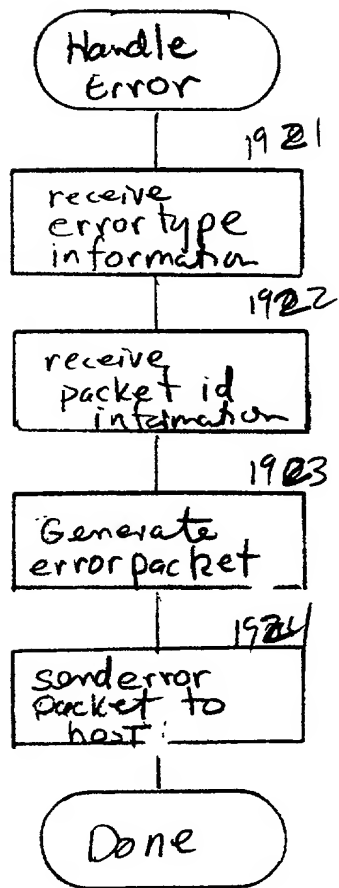


Fig 19B



19C

8b Code	9 bit symbol
0000 0000	101010101
0000 0001	101010100
0000 0010	101010111
⋮	
0101 0101	001010101
⋮	
0111 0110	001110110
0111 0111	100100010
⋮	
1111 1111	110101010

Fig 20

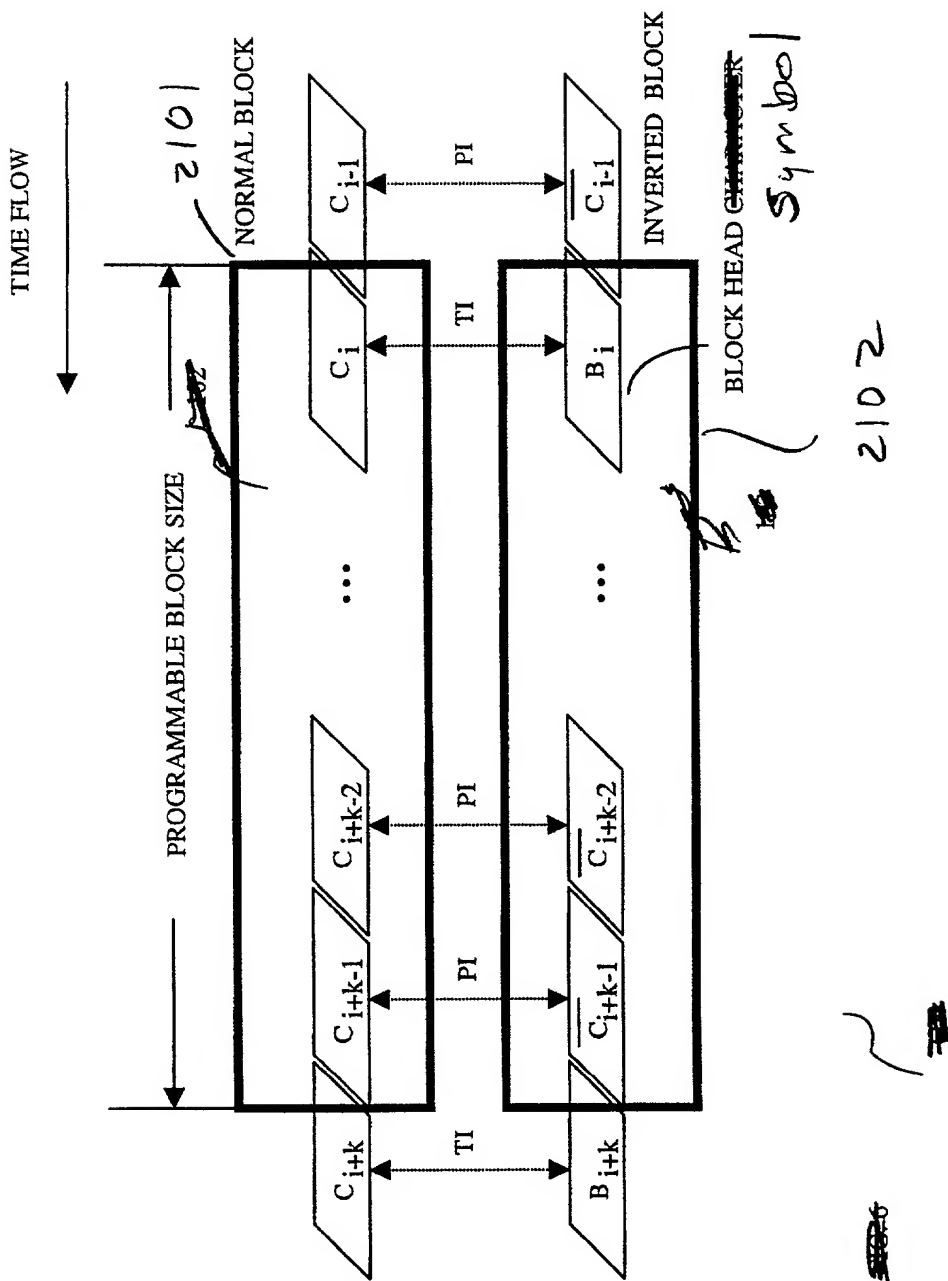


Fig 21B

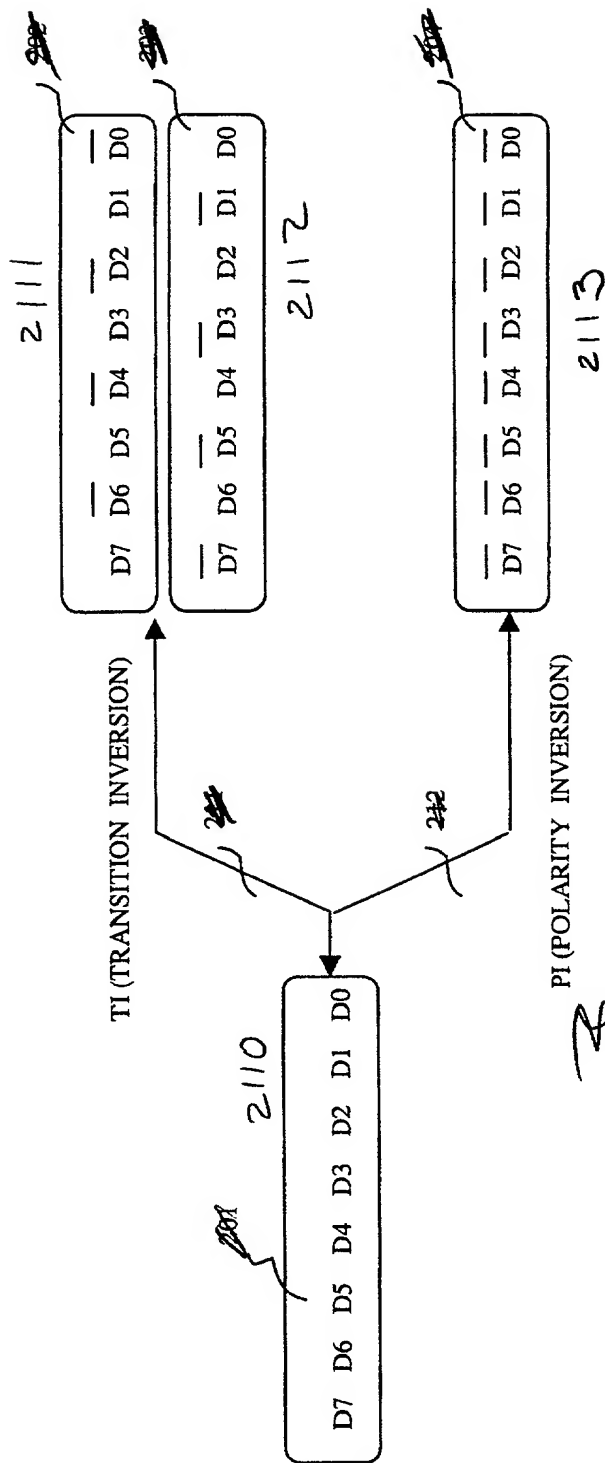


FIG. 21C

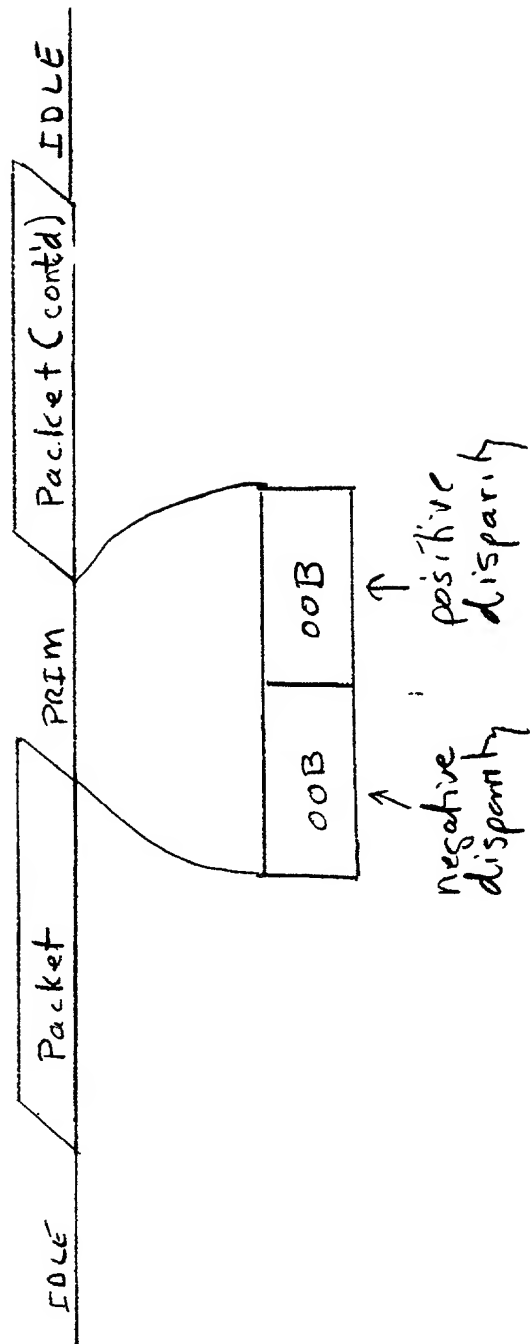


Fig 22

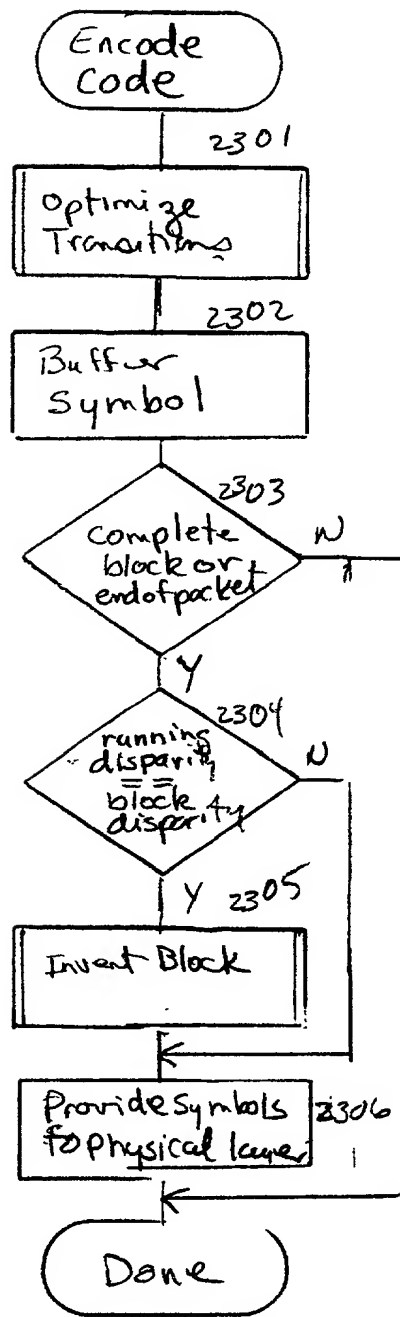


Fig 23

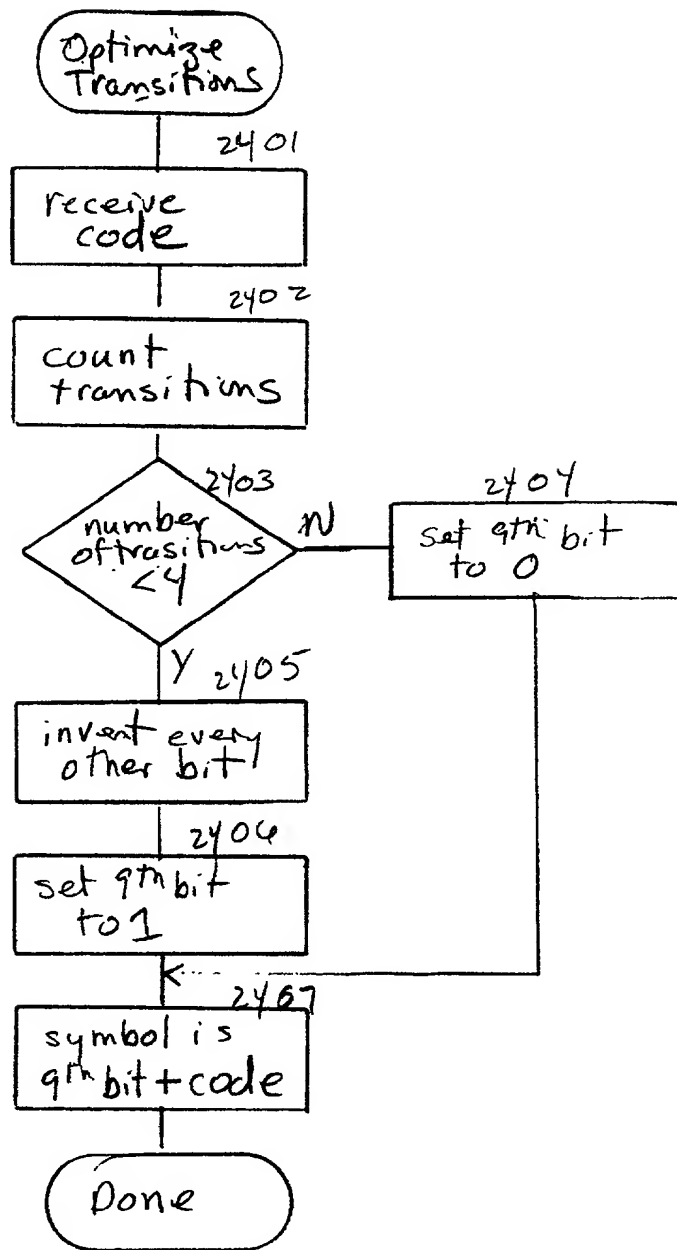


Fig 24

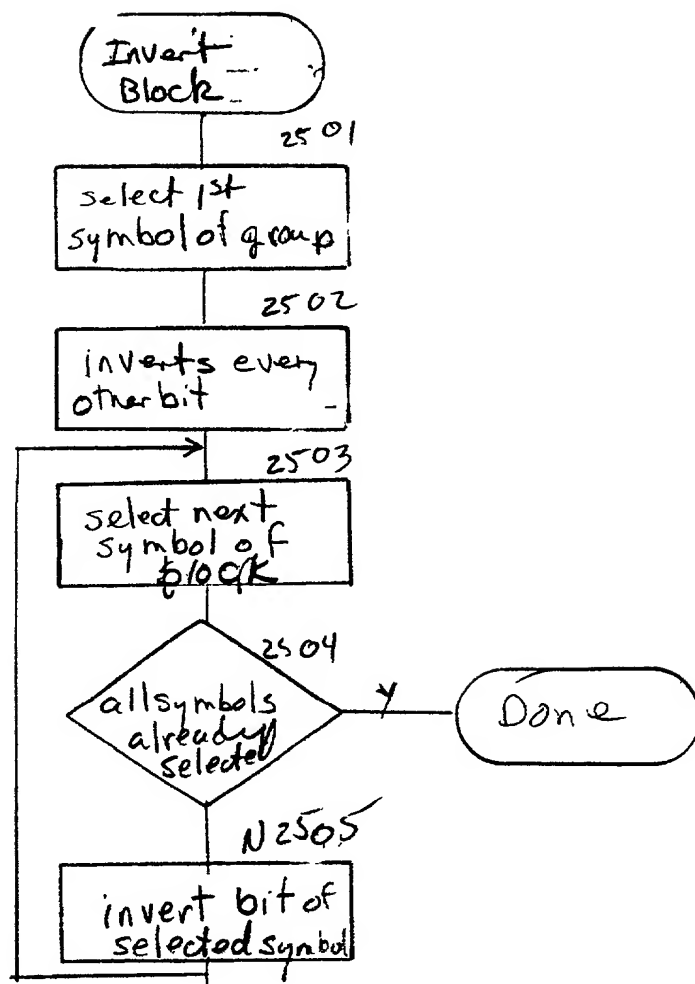


Fig 25

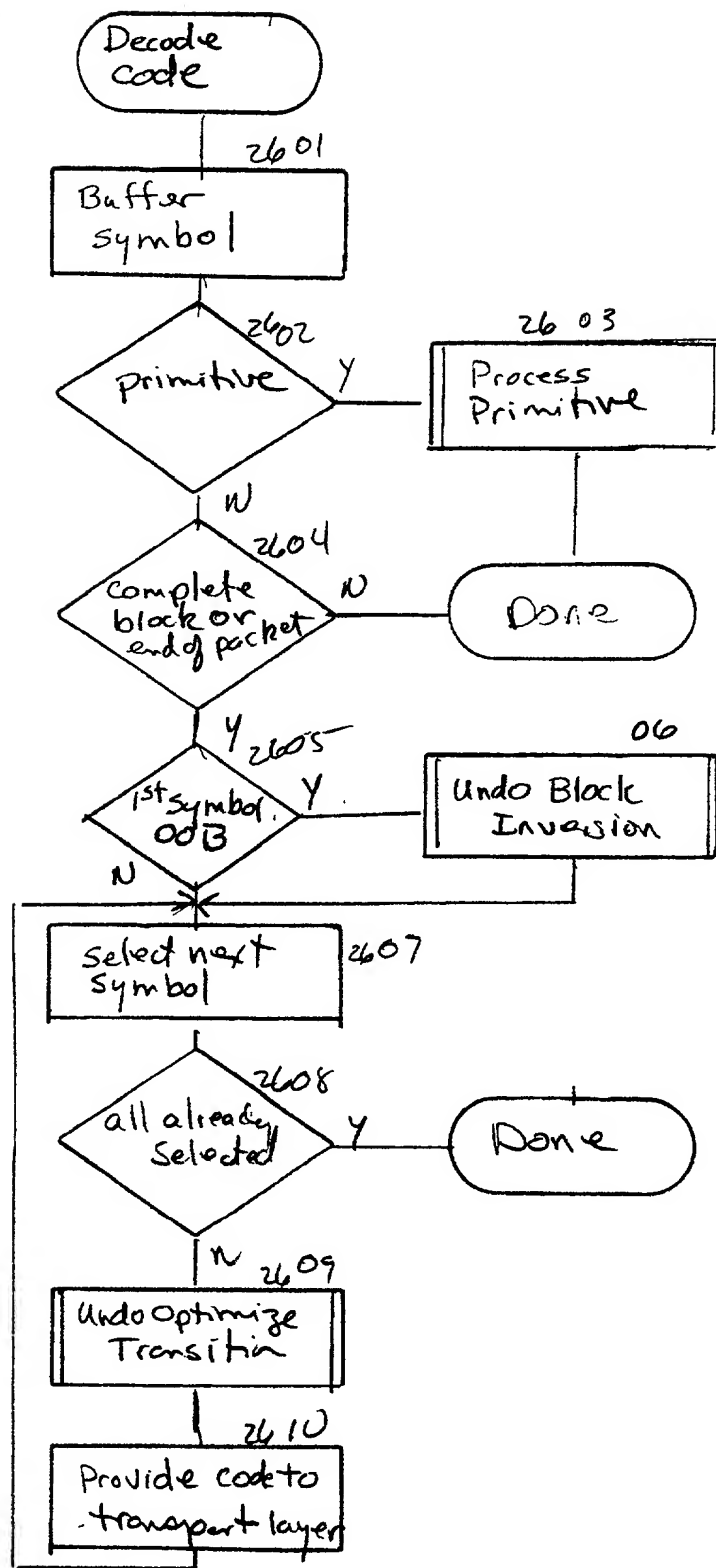


Fig 26

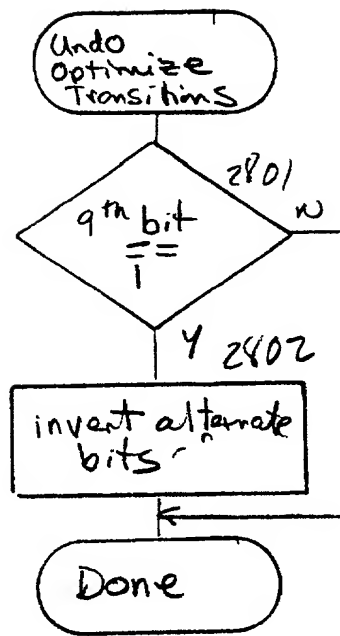


Fig 28

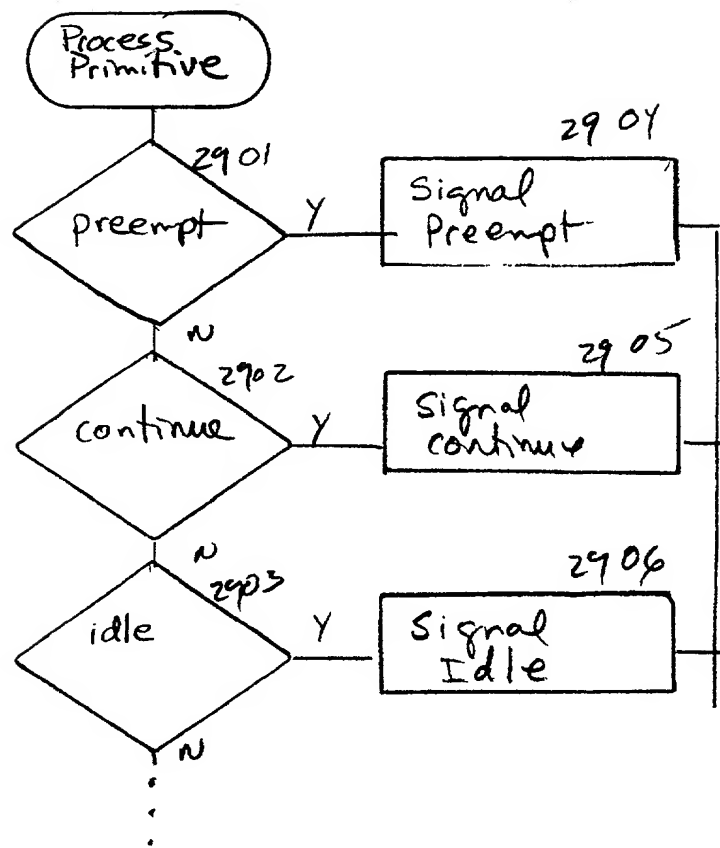


Fig 29

Multiport Memory Device 3000

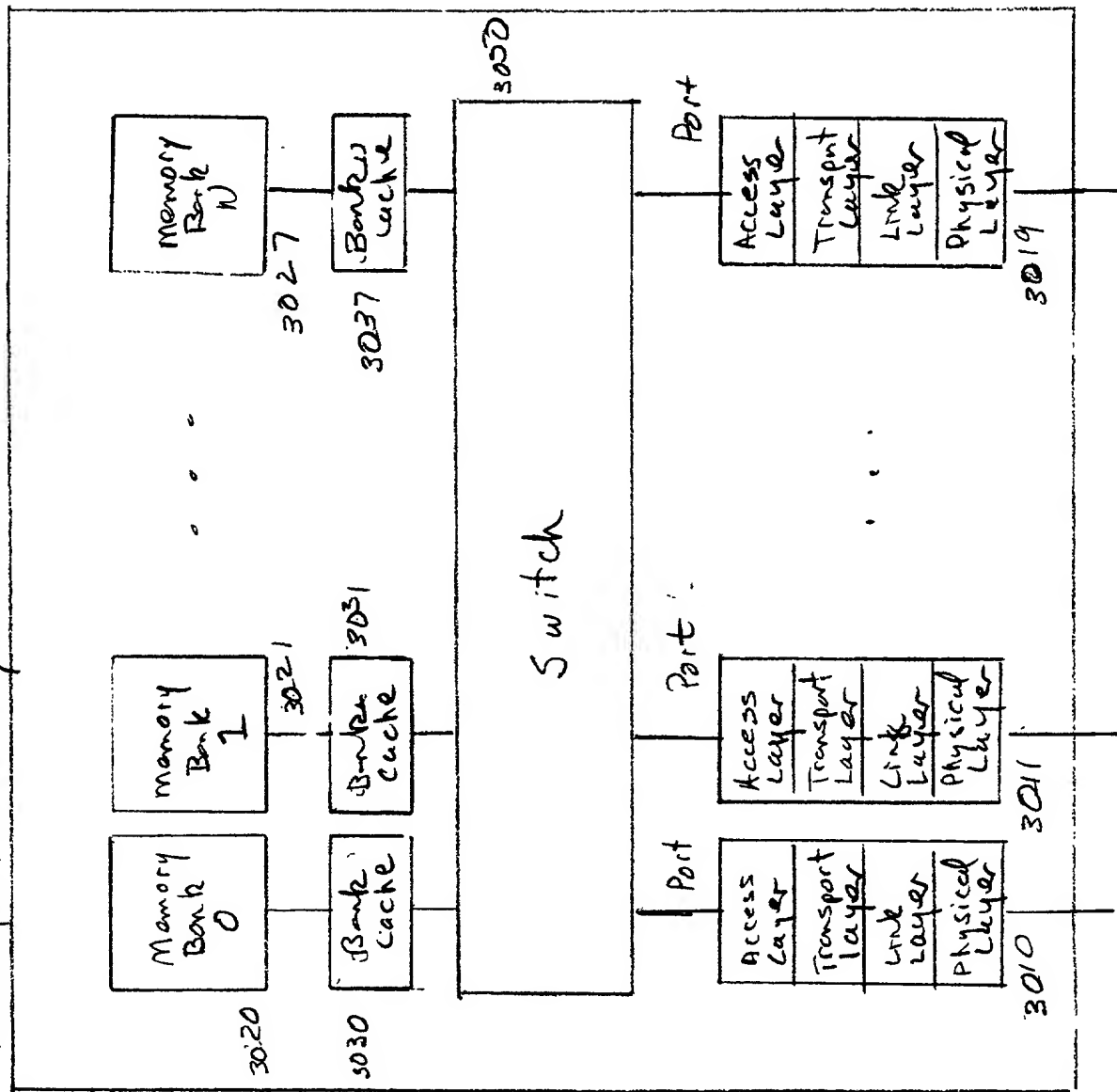


Fig 30